

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU  | EXAMINER    |
|----------|-------------|-------|----------|------|-------------|
| 10042520 | 01/09/2002  | 438   | 316      | 2825 | KESHAVAN, B |

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\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

FRANCE 0100691 01/18/2001

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|--|--|----------------------------------|--|
| PG-PUB   | DO NOT PUBLISH <input type="checkbox"/>                  | RESCIND <input type="checkbox"/> |  |
| Foreign priority claimed                       | <input type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO               |  |
| 35 USC 119 conditions met                      | <input type="checkbox"/> yes <input type="checkbox"/> no | 00GR32554365                     |  |
| Verified and Acknowledged Examiners's initials |  |                                  |  |
| TITLE : Dram memory integration method         |  |                                  |  |

U.S.DEP'T. OF COMM /PAT.& TM-PTO-436L(Rev. 12-94)

|  |           |                      |                     |
|--|-----------|----------------------|---------------------|
| NOTICE OF ALLOWANCE MAILED   |           | CLAIMS ALLOWED       |                     |
|  |           | Assistant Examiner   |                     |
|  |           | Total Claims         | Print Claim for O.G |
| ISSUE FEE  |           | DRAWING              |                     |
| Amount Due   | Date Paid | Sheets Drwg.         | Figs.Drwg.          |
|  |           | Print Fig.           |                     |
| TERMINAL   |           | Primary Examiner     |                     |
| DISCLAIMER   |           | Application Examiner |                     |
| PREPARED FOR ISSUE   |           |                      |                     |
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